

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No.: US 7,437,512 ) Mail Stop MF  
Issue Date.: October 14, 2008 )  
Application No.: 10/788,725 )  
Filing Date.: February 26, 2004 )  
Inventors: Michael W. MORROW )  
For: LOW POWER SEMI-TRACE )  
INSTRUCTION CACHE )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**FEE ADDRESS FOR MAINTENANCE FEE PURPOSES**  
**IN ACCORDANCE WITH 37 C.F.R. 1.363**

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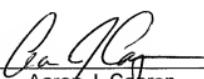
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Respectfully submitted,

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Date: November

4, 2008

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